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CLAIMS: Please amend the claims according to the status designations in the following list, which contains all claims that were ever in the patent application, with the text of all authorises.

1. (withdraw).

What I claim are:

2 (withdraw).

3. (withdraw).

5. (will:draw).

4. (withdraw).

6. (wittdaw).

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7. (withdraw).

8. (withdraw).

9. (wiff-draw).

16. (withdraw).

11. (withdraw).

13. (withdraw).

13. (withdraw).

14. (Currently amended) A vertical serifoondustry thip or device comprising

a) an electrically conductive submount chip;

an epitakial layer comprising a secund confinement layer, an active layer, and a first

confinement layer stacked on said submount chip;

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reflective and Ohmic contact layers sandwiched between seid epitaxial layer and seid

sutmount chip;

d) a patterned contact pad dispos€C on said first confinement beyor where:n said parterned

contact pad comprising at least one wire bonding pad.

15. (withchaw).

16. (Curently amended) The vertical semiconductor cuts or device of claim 14 further

comprising a current spreading layer sardwiched between said putterned contact pad and said

first confinement leyer.

17. (withdraw)

18. (Curently arrended) The vertical semiconductor chip or device of cluim 14 wherein said

patterned contact pad is a grid-ring-shaped contact pad with at least one wire bonding pad.

19. (withdraw).

20. (withdraw).

21. (withdraw).

22. (withdraw).

25. (withdraw).

24. (withdraw).

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